



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2814
Examiner: Anh D. Mai

TECHNOLOGY CENTER 2800
AUG 13 2002

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2800
13/C(NE)
1-24-02
8-19-02
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In Re PATENT APPLICATION Of:

Applicant : Hiroki NAKAMURA)

Serial No. : 09/625,178)

Filed : July 25, 2000)

For : SEMICONDUCTOR DEVICE HAVING)
WIRING PATTERNS AND DUMMY)
PATTERNS COVERED WITH)
INSULATING LAYER (twice amended))

AMENDMENT
AFTER
FINAL ACTION

Attorney Ref. : F98ED0762)

August 9, 2002

BOX: AF
Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Examiner's Office Action mailed June 11, 2002, please

amend the above-identified application as follows:

Please
Do not Enter
AM 8/10/02